

RESUME

Dr. Ganga Ram Mishra
Professor
Department of Physics & Electronics
Dr. RML Avadh University Ayidhya

Mob: 09451805326
E-mail: grmishra@gmail.com
gr_mishra@rediffmail.com

Area of Interest : Digital System Design, Microprocessor Based System Design, Embedded Systems, Signal Processing and VLSI Design, Reversible Logic Design

Present Position

- Working as Professor Department of Physics & Electronics, Dr. RML Avadh University, Ayodhya from 20 June 2020.

Experience

- Worked with Amity University Lucknow Campus from 5th Oct, 2006 to 19 June 2020 in Department of Electronics, Amity School of Engineering & Technology, Amity University Uttar Pradesh Lucknow campus. The details of services are as under:

S.No.	Position Held	Period	
		From	To
1	Assistant Professor Grade-II	01/09/2014	19/06/2020
2	Assistant Professor	01/07/2011	31/08/2014
3	Sr. Lecturer	01/07/2010	30/06/2011
4	Lecturer	01/06/2007	30/06/2010
5	Adhoc Lecturer	05/10/2006	31/05/2007

Other Experience

- Worked with ITM GIDA Gorakhpur from 1st Aug. 2003 to 4th Oct. 2006.
- Worked with GTBIT New Delhi from 23rd April 2002 to 31th July 2006.
- Worked with RGEC Meerut New Delhi from 14th July 2001 to 23rd Feb2002.

Project/Thesis/Research Guidance/Supervision at Amity University Lucknow Campus:

- U.G Level (B.Tech) : 30 Groups
- P.G Level (M.Tech) : 12 Students
- Ph.D : 02 Students enrolled (As Co- Supervisor)
: 01 Students enrolled (As Supervisor)
: 01 Student Awarded Degree (As Co-Supervisor)
: 01 Student Submitted Thesis (As Co-Supervisor)

Ph.D Degree Awarded/Submitted as Co-Guide:

- Ms. Vandana Shukla on topic “Design and Implementation of Reversible Logic Circuit” as Co-Guide and Degree Awarded in 2018.
- Mr. Sunil Kumar Ojha on topic “Analysis and Optimized Design of Semiconductor Memory Cell” as Co-Guide (Thesis Submitted).

Lab Established at Amity University Lucknow Campus:

- Setup Microprocessor Lab, Digital Electronics, Circuit & System Lab, Microcontroller Lab, MATLAB Programming Lab, DSP Lab, Embedded System Lab, Embedded Microcontroller Lab, Advanced Microcontroller Lab.

Administrative/Academic Committee at Amity University Lucknow Campus

- Worked as Member Examination Control Committee from 2010 to December, 2019.
- Worked as Program Leader M.Tech (Embedded System & Technology) First Year.
- Worked as Mentor B.Tech (ECE)/ M.Tech (ECE/EST) students.
- Worked as Coordinator P.I Board of U.G. / P.G. Admission Test of Amity School of Engineering & Technology from 2008 to July 2019.
- Worked as Member, Accreditation Document Preparation Team for NAAC/WASC/IET (U.K)
- Worked as Coordinator, Alumni, Department of Electronics & Communication Engineering, Amity School of Engineering & Technology, Amity University Lucknow Campus from 2010 to 19 June 2020.
- Worked as Member Result Counseling Committee, Amity School of Engineering & Technology, Amity University Lucknow Campus from 2015 to 2019.
- Worked as Member Departmental Research Committee (DRC), Amity School of Engineering & Technology, Amity University Lucknow Campus from 2018 to 19 June 2020.
- Worked as Member Students Research Committee (SRC), Amity School of Engineering & Technology, Amity University Lucknow Campus from 2011 to 19 June 2020.
- Worked as Member Secretary Research Program /Ph. D Coordinator, Amity School of Engineering & Technology, Amity University Lucknow Campus from 2015 to 19 June 2020.

Extra-Curricular Activities at Amity University Lucknow Campus

- Taken Initiative for establishment of IETE Students Forum (ISF) in Amity School of Engineering & Technology, Amity University Lucknow Campus.
- Organized Industrial Training of U.G/P.G. Students of Department of Electronics & Communication Engineering, Amity School of Engineering & Technology, Amity University Lucknow Campus in RDSO, Doordarshan, BSNL, NIELIT and MNNIT Allahabad.
- Organized free of cost Industrial visit of B.Tech ECE VII Semester Students (Batch 2010-2014) at BSNL Mahanagar, Lucknow.
- Organized free of cost Industrial visit of B.Tech ECE IV Semester Students (Batch 2013-2017) and M.Tech (ECE/EST) II Semester Students (Batch 2014-2016) at BSNL Kesharbag Lucknow.

- Organized free of cost Industrial visit of B.Tech ECE IV Semester Students (Batch 2014-2016) and M.Tech (ECE/EST) II Semester Students (Batch 2015-2017) at BSNL RTTC, Lucknow.
- Collaborated CSIR-CIMFER Dhanbad and SAMEER Kolkata for M.Tech (ECE/EST) final year project.

Interaction with Other Related Establishments

- Member Accreditation Committee of NIELIT –O/A Level Hardware and Software Courses (Formerly DOEACC Courses) NIELIT (Formerly DOEACC SOCIETY) Dept. of Electronics, Ministry of I.T. Gvt. of India.
- Member Selection Committee of NIELIT (Formerly DOEACC SOCIETY) Chandigarh Centre, Lucknow Branch, Dept. of Electronics, Ministry of I.T. Gvt. of India for Faculty position on contact basis.
- Member Selection Committee of NIELIT (Formerly DOEACC SOCIETY) Chandigarh Centre, Lucknow Branch, Dept. of Electronics, Ministry of I.T. Gvt. of India for Computer operators on contact basis.
- Member Executive Committee, Institution of Electronics & Telecommunication Engineers (IETE) Lucknow Centre (20018-2020)
- Special Invitee Member Executive Committee, Institution of Electronics & Telecommunication Engineers (IETE) Lucknow Centre (20012-2014 and 2016-2018).
- Returning Officer, Institution of Electronics & Telecommunication Engineers (IETE) Lucknow Centre Elections 20016-2018.
- Member Executive Committee, Institution of Electronics & Telecommunication Engineers (IETE) Lucknow Centre (20012-2014 and 2014-2016).
- Co-opted member of Executive Committee, Institution of Electronics & Telecommunication Engineers (IETE) Lucknow Centre (20010-2012).
- Member Executive Committee, Institution of Electronics & Telecommunication Engineers (IETE) Lucknow Centre (2008-2010).
- Member Electronics & Communication Committee, Institution of Electronics & Telecommunication Engineers (IETE) Lucknow Centre (2010-2012).
- Member Education Committee, Institution of Electronics & Telecommunication Engineers (IETE) Lucknow Centre from 2008 to till date.
- Nominated as Coordinator and Center nominee of IETE Students' Forum (ISF), Amity School of Engineering & Technology, Amity University Lucknow Campus.
- Founder Member Professional Activity Centre IETE Gorakhpur.

Publication in Journals:

- Anand Ranjan, O.P. Singh, G.R. Mishra, Himanshu Katiyar, "Optimal Relay Node Selection Using Multi-objective based Lion Optimization Algorithm for Multiuser Cooperative Communication Network" International Journal of Advanced Science and Technology (A **Scopus Indexed Journal**), 29(05), 2020, PP. 11867-11884
- Janak Kapoor, GR Mishra, Manish Rai, "Adaptive Least Mean Square Noise Cancellation Model Using Various Fixed Coefficient Digital Filters" International Journal of Advanced Science and Technology, (A **Scopus Indexed Journal**), Vol. 29 No. 10s , 2020 , pp. 8448-8455..

- Janak Kapoor, GR Mishra, Manish Rai, “Echo Cancellation System with Dual Adaptive Filter and Effect of Multiplication Factor in LMS Algorithm Weight Equation” *Test Engineering and Management*, (**A Scopus Indexed Journal**), Volume 82, 2019, pp. 4102-4108.
- Sunil Kumar Ojha, O.P.Singh, G.R.Mishra, P.R.Vaya “Performance Evaluation of Low-Power 4 X 4 Bit Memory Array Multiplier” *International Journal of Engineering Research and Technology*, (**A Scopus Indexed Journal**), Volume 12, Number 10 (2019), pp. 1764-1768.
- Vandana Shukla, O. P. Singh, G. R. Mishra, R. K. Tiwari, “Reversible Realization of 4-Bit Vedic Multiplier Circuit with Optimized Performance Parameters”, *Sensor Letters*, American Scientific Publishers (**A Scopus Indexed Journal**), Volume 17, Number 10, pp. 826-831(6) , October 2019.
- Raj Kumar Tiwari, Gaya Prasad, Monika Tiwari and Ganga Ram Mishra “Design and Development of CMOS Oscillator Using RKTG Pair Amplifier” *International Journal of Engineering Research and Technology American International Journal of Research in Science, Technology, Engineering & Mathematics*, March-May 2019 issue, Issue 26: Volume 1, pp. 90-92.
- Raj Kumar Tiwari, Gaya Prasad, Shiksha Jain, Ganga Ram Mishra, Monika Tiwari and Parul Trivedi “RKTG Pair Amplifier with Gain Boosting Stage” *International Journal of Engineering Research and Technology American International Journal of Research in Science, Technology, Engineering & Mathematics*, 25(1), December 2018-February 2019, pp. 23-26.
- Sunil Kumar Ojha, O.P.Singh, G.R.Mishra, P.R.Vaya “An Effect Use of Memory Grouping Algorithm for Implementation of BIST in Self-Test” *Journal of Engineering and Applied Sciences (JEAS)* (**A Scopus Indexed Journal**), 14(8), P.P 20192695-2700.
- Sunil Kumar Ojha, O.P.Singh, G.R.Mishra, P.R.Vaya “Effect of Threshold Roll-Off on Static Noise Margine on SRAM Cell” *Journal of Engineering and Applied Sciences (JEAS)* (**A Scopus Indexed Journal**), 13(14), 2018, PP: 5801-5806.
- Prashant Kumar , Prabhat Chandra Shrivastava, Manish Tiwari, Ganga Ram Mishra, “High-Throughput, Area-Efficient Architecture of 2-D Block FIR Filter Using Distributed Arithmetic Algorithm” *Circuits System Signal Processing*, Published by Springer (**A SCI Indexed Journal**), Vol. 162, Issue. 6, July, 2018, Page No. 1-15
- Vandana Shukla, O. P. Singh, G. R. Mishra, R. K. Tiwari, “ Design and Analysis of Reversible Realization of Vedic Multiplier Circuit ” *Sylwan*, (**A SCI Indexed Journal**), Vol. 162, Issue. 6, 2018, Page No. 32-43.
- Raghuvendra Pratap Tripathi, G.R.Mishra, “Design and Implementation of Smart System for Assistance of Sleepy Driver Using ECE EEG and other Physiological Signals *International Journal of Scientific Research in Science and Technology (IJSRST)*, Volume 4, Issue 8, 2018, Pages 179-187.
- Vandana Shukla, O. P. Singh, G. R. Mishra, R. K. Tiwari, “Reversible Realization of N-bit Arithmetic Circuit for Low Power Loss ALU Applications”, *Procedia Computer Science*, Elsevier (**Scopus Indexed**), Volume 125, 2018, Pages 847-854.
- Vandana Shukla, O. P. Singh, G. R. Mishra, R. K. Tiwari, “Reversible Realization of Common Bus Structure for ALU Applications”, *Journal of Engineering and Applied Sciences (JEAS)* (**A Scopus Indexed Journal**), 12(11), 2017, PP: 2984-2989,.
- Vandana Shukla, O. P. Singh, G. R. Mishra, R. K. Tiwari, “Realization of Optimized Eight- Bit Binary Shifter using Reversible Logic Approach”, *International Journal of Services Technology and Management* (**A Scopus Indexed Journal**), Vol. 23, No. 4, 2017, PP: 262-274,..
- Rajinder Tiwari, G. R. Mishra, R. K. Tiwari, “A Novice Approach of Design of CMOS based Comparator using 0.18 μm CMOS Technology for ASP Applications *Sylwan*, (**A SCI Indexed Journal**), Vol. 160, Issue. 1, Jan 2016, Page No. 330-341.

- Swati Mishra, G.R Mishra, Rajinder Tiwari “A Study on Digital Watermarking and Application for Copyright Protection in Digital Image”, International Journal of Electrical & Electronics Engineers (IJEET), Vol.8, Issue 1, January-June 2016, Page No. 389– 396.
- Swati Mishra, G.R Mishra, “Application of Watermarking Techniques in Medical Data”, International Journal of Electrical & Electronics Engineers (IJEET), Vol.8, Issue 1, January-June 2016, Page No. 991– 996.
- Shweta Singh, G. R. Mishra, “Performance Analysis of Cognitive Radio Over Gaussian Channel”, International Journal of Advanced Technology in Engineering and Science, Vol. No. 4, Issue No. 05, May 2016, Page No. 148– 153.
- Rajinder Tiwari, G. R. Mishra, “A Novice Approach of Designing CMOS Based Switchable Filters for ASP Applications”, International Journal of Electrical and Computer Engineering (IJECE), Vol 5, No 6: December 2015, (**A Scopus Indexed Journal**), Page No. 1354-1362.
- Vandana Shukla, O. P. Singh, G. R. Mishra, R. K. Tiwari, “Design and Implementation of Four Bit Binary Shifter Circuit Using Reversible Logic Approach”, International Journal of Reconfigurable and Embedded Systems (IJRES), Vol. 4, No. 3, November 2015, Page No. 213– 218.
- Vandana Shukla, O. P. Singh, G. R. Mishra, R. K. Tiwari, “A Novel Approach to Design 2-Bit Binary Arithmetic Logic Unit (ALU) Circuit using Optimized 8:1 Multiplexer with Reversible Logic”, Journal of Communications Software and Systems Vol. 11, No.2 June, 2015, (**A Scopus Indexed Journal**), Page No. 104-109.
- Vandana Shukla, O. P. Singh, G. R. Mishra, R. K. Tiwari, “Novel Approach to Design a 4-Bit Binary Comparator Circuit with Reversible Logic using CDSM Gate”, International Journal of Business Data Communications and Networking, 11(1), 36-49, January-March 2015, (**A Scopus Indexed Journal**), Page No. 330-341.
- Vandana Shukla, O. P. Singh, G. R. Mishra, R.K.Tiwari “An Optimized Circuit of 8:1 Multiplexer Circuit using Reversible Logic Gates”, Special Issue of International Conference on Communication, Computing and Information Technology (ICCCMIT-2014) published by International Journal of Computer Applications, Page No. 17-20.
- Monika Tiwari, G. R. Mishra, Vineet Tiwari, “Design of Reversible Logic Based One Bit Memory Cell Circuit for Low Voltage High Speed Applications”, International Journal of Research in Electronics & Communication Technology Volume-2, Issue-3, May-June, 2014, Page No. 84-88.
- Monika Tiwari, G. R. Mishra, Vineet Tiwari, “A New Circuit Model for Low Voltage High Speed CMOS Push-Pull Amplifier”, International Journal of Research in Electronics & Communication Technology, Volume-2, Issue-3, May-June, 2014, Page No. 51-55.
- Sunil Kumar, O. P. Singh, G. R. Mishra, P. R. Vaya, “Design of a Novel Low-Power SRAM Column”, Global Journal of Research in Engineering (F), Volume 14 Issue 5 Version 1.0 Year 2014, Page No. 45-51.
- Mudit Shukla, G.R.Mishra, “DAPSO and PSO-VAF in Linear Phase Digital Low Pass FIR Filter Design”, Circuits and Systems, 2014, 5, Page No. 57– 67.
- Mudit Shukla, G. R. Mishra, O. P. Singh, Sachin Kumar, “Linear Phase Digital Low Pass FIR Filter Design by Attractive and Repulsive Particle Swarm Optimization”, ACEEE Int. J. on Communication, Vol. 5, No. 1, March 2014, Page No. 13-19,
- Anchal Verma, O. P. Singh, Ganga Ram Mishra, “Analysis of Feeding Mechanism in Microstrip Patch Antenna”, International Journal of Research in Engineering and Technology Volume: 03 Issue: 04 | Apr-2014, Page No. 786-792.

- Ankit Dixit, O.P. Singh, G. R. Mishra, “Design and Analysis of a Patch Antenna for Bluetooth Application”, International Journal of Research in Engineering and Technology Volume: 03 Issue: 05 | May-2014, Page No. 1-5.
- Priti Tiwari, G R Mishra, “Simulation OF Quarter Car Model”, IOSR Journal of Mechanical and Civil Engineering (IOSR-JMCE) Volume 11, Issue 2 Ver. III (Mar- Apr. 2014), Page No. 85-88.
- G.R. Mishra, Mudit Shukla, O.P. Singh, Sachin Kumar, Kamal Ahmad, “Parametric Optimization and Analysis of Adaptive Equalization Algorithms for Noisy Speech Signals”, IOSR Journal of Electrical and Electronics Engineering (IOSR-JEEE) Volume 4, Issue 6 (Mar. -Apr. 2013), Page No.69-74.
- Rajkumar Tiwari, Sachin Kumar, G. R. Mishra, “A Class AB CCII Topology Based on Differential Pair With Modified Output Stage”, International Journal of Electrical Engineering and Technology (IJEET), Volume 4, Issue 1, January- February (2013), Page No. 68-74.
- G.R. Mishra,, Mudit Shukla, Saurabh Kumar Mishra, O. P. Singh, Sachin Kumar, “A Comparative Study of Particle Swarm Optimization and Genetic Algorithm”, International Journal of Advanced Scientific and Technical Research, Issue 3 volume 2, March-April 2013, pp. 98-103.
- Chanchal Srivastava, Saurabh Kumar Mishra, Pallavi Asthana, G. R. Mishra4, O.P. Singh, “Performance Comparison of Various Filters and Wavelet Transform for Image De-Noiseing”, IOSR Journal of Computer Engineering (IOSR-JCE), 8727Volume 10, Issue 1 (Mar. - Apr. 2013), PP 55-63
- G. R. Mishra, Saurabh Kumar Mishra, Akanksha Trivedi, O.P. Singh, Satish Kumar, “Improving the Efficiency of Spectral Subtraction Method by Combining it with Wavelet Thresholding Technique”, International Journal of Research in Computer Science, Volume 3 Issue 3 (2013) pp. 29-33
- Raj Kumar Tiwari, Sachin Kumar, G.R. Mishra, “A Modified Approach to Noise Analysis in CCII”, International Journal of Electronics Engineering (IJEE) Vol.4, No.2 of December 2012. Bulletin of Pure & Applied Sciences, Issue I, Jan- June 2013. PP 23-35
- Vandana Shukla, O. P. Singh, G. R. Mishra, R. K. Tiwari, “Novel Design of 4:1 Multiplexer Circuit Using Reversible Logic”, International Journal of Computational Research, Vol. 03, Issue, 10, October 2013, Page No. 30-35.
- Raj Kumar Tiwari, Sachin Kumar, G.R. Mishra, “A High Performance Novel PMOS Wilson Current Mirror”, International Journal of Electronics Engineering (IJEE) Vol.4, No.2 of December 2012, PP 173– 176.
- Raj Kumar Tiwari, Sachin Kumar, G.R. Mishra, “A Study on Techniques of Improvement in Current Mirror Using Wilson Scheme”, International Journal of Electronics and Communication Engineering & Technology (IJECET), Volume 3, Issue 2, July- September 2012, PP 56– 62
- O.P.Singh, G.R.Mishra, Aditi Kumar, “Image Compression Using Back-Propagation Algorithms”, International Journal of emerging Trends in engineering & Development, Issue 2, Volume 7, (Nov 2012), pp. 240-244
- Vandana Shukla, O. P. Singh, G. R. Mishra, R. K. Tiwari, “ Design of a 4-bit 2’s Complement Reversible Circuit for Arithmetic Logic Unit Applications” Special Issue of International Journal of Computer Applications (0975 – 8887) The International Conference on Communication, Computing and Information Technology (ICCCMIT) 2012, pp. 1-5
- Satish Kumar, O. P. Singh, G. R. Mishra, Saurabh Kumar Mishra, Akanksha Trivedi, “Speech Compression and Enhancement using Wavelet Coders”, International Journal of Electronics Communication and Computer Engineering Volume 3, Issue 6, 2012, ISSN (Online): 2249–071X, ISSN (Print): 2278–4209, pp. 1169-1173
- O.P. Singh, K.K Singh, G.R. Mishra, Deepak Tiwari, Saumya Dubey, “Analysis of ECG Signaling Using Wavelet Transform” International Journal of Engineering Innovation & Research, Volume 2, Issue 1,2012, (ISSN: 2277 – 5668), pp. 71-74

- Vandana Dubey, O.P. Singh, G.R. Mishra, “Design and Implementation of a Two-Bit Binary Comparator Using Reversible Logic”, International Journal of Scientific and Research (IJSR) Volume 2, Issue 7, July 2012, pp. 01-04
- O.P Singh, Satish Kumar, G R Mishra, Charu Pandey, Vartika Singh, “Study of Watermarking Techniques Used in Digital Image”, International Journal of Scientific and Research Publications, Issue 10, Volume 2, Oct 2012, pp. 1-5
- Rajinder Tiwari, R. K. Singh, Ganga Ram Mishra, “A New Approach for Design of CMOS Based Cascode Current Mirror for ASP Applications”, International Journal of Electronics and Communication Engineering & Technology (IJECET) Volume 2, Issue 2, May – July (2011), pp. 01-07
- Rajinder Tiwari, R.K.Singh, Ganga Ram Mishra, “Technical Development and Applications of Nanoscale MOSFET in Analog CMOS Circuits: A Brief Review ”, Journal of Physical Science, Vol.2 (No.1), pp. 143-149, 2010.
- Manish Mishra, G.R.Mishra, N.K.Chaudhary, Vikas Tripathi, “Design and Implementation of SD2 Fast Adder for Fast DSP Processor”, JUSPS Vol.22, No.2, pp 259-264, 2010.
- Raj Kumar Tiwari, Ganga Ram Mishra & Maheshwar Misra “A New High Performance Low Voltage CMOS Differential Amplifier”, JUSPS Vol.21, No.3, Dec. 2009.
- Ganga Ram Mishra and Raj Kumar Tiwari “A High Performance Low Voltage Level Shifted Cascode Current Mirror” JUSPS, Vol. 21, Jun-Apr 2009.
- R.K.Tiwari and Ganga Ram Mishra “Comparative Studies on CMOS and BJT Current Mirror Circuit” Bulletin of Pure & Applied Sciences Vol. 27D.

Publication in IEEE Conference/Seminar Proceeding:

- Vandana Shukla, O. P. Singh, G. R. Mishra, R. K. Tiwari, “Design and Performance Analysis for the Reversible Realization of Adder/Subtractor Circuit”, International Conference on Emerging Trends in Computing and Communication Technologies (ICETCCT), Published by IEEE Xplore, 2017, PP 1-7.
- Janak Kapoor, Ganga Ram Mishra, Manish Rai, “Characteristics and Properties of Audio Signal and Noise Cancellation Techniques: A Theoretical Review”, International Conference on Emerging Trends in Computing and Communication Technologies (ICETCCT), Published by IEEE Xplore, 2017, PP 1-7.
- R.P Tripathi, A Tiwari, G.R Mishra, “Design and Fabrication of a Nano Patch Electrode for ECG using CNT/PDMS”, International Conference on Computing Methodologies and Communication (ICCMC), 2017, Published by IEEE Xplore, 2017, PP 335-339.
- R.P Tripathi, G.R Mishra, “Design and Implementation of a Real Time Stress Monitoring System with the help of ECG using Matlab Tool”, International Conference on Computer, Communications and Electronics (Comptelix) 2017, Published by IEEE Xplore, 2017, PP 365-369.
- R.P Tripathi, G.R Mishra, “Study of Various Data Compression Techniques used in Lossless Compression of ECG Signals”, International Conference on Computing, Communication and Automation (ICCCA) 2017, Published by IEEE Xplore, 2017, PP 1093-1097
- Sunil Kumar Ojha, OP Singh, GR Mishra, PR Vaya, “Analysis and Design of Single Ended SRAM Cell for Low-Power Operation”, 11th International Conference on Industrial and Information Systems (ICIIS) 2016, Published by IEEE Xplore, 2018, PP 178-181,.
- Vandana Shukla, O. P. Singh, G. R. Mishra, R. K. Tiwari, “Performance Parameters Optimization and Implementation of Adder/Subtractor Circuit Using Reversible Logic Approach”, 11th International Conference on Industrial and Information Systems (ICIIS) 2016, Published by IEEE Xplore, 2018, PP 323-328

- Vandana Shukla, O. P. Singh, G. R. Mishra, R. K. Tiwari, “A Novel Approach for Reversible Realization of 8-Bit Adder- Subtractor Circuit with Optimized Quantum Cost”, IEEE Explore, 2016.
- Vandana Shukla, O. P. Singh, G. R. Mishra, R.K.Tiwari “Application of CSMT Gate for Efficient Reversible Realization of Binary to Gray Code Converter Circuit”, 2015 IEEE UP Section Conference on Electrical Computer and Electronics (UPCON), Published by Published by IEEE, Page No1-5, ISBN No: 978-1-4673-8506-0.
- Vandana Shukla, O. P. Singh, G. R. Mishra, R.K.Tiwari “A Novel Approach to Design a Redundant Binary Signed Digit Adder Cell Using Reversible Logic Gates”, 2015 IEEE UP Section Conference on Electrical Computer and Electronics (UPCON), Published by Published by IEEE, Page No1-6, ISBN No: 978-1-4673-8506-0.
- Vandana Shukla, O. P. Singh, G. R. Mishra, R.K.Tiwari “A Novel Approach to Design Decimal to BCD Encoder with Reversible Logic”, Proceedings of International Conference on Power, Control and Embedded Systems (ICPCES), 2014, Published by IEEE Explore, Page No1-5, ISBN No: 978-1-4799-5910-5.

Publication in National Conference/Seminar Proceeding:

- Vandana Shukla, O. P. Singh, G. R. Mishra, R.K.Tiwari “Static Noise Margin Analysis of an SRAM Cell”, Proceeding of International Conference on Computing, Communication and Control Technology (IC4 T-2016), Page No 84-86, ISBN No: 978-93-5258-814-5.
- Vandana Shukla, O. P. Singh, G. R. Mishra, R.K.Tiwari “Reversible Realization of a 4-Bit Arithmetic Logic Unit (ALU) Circuit”, Proceeding of International Conference on Computing, Communication and Control Technology (IC4 T-2016), Page No 113-117, ISBN No: 978-93-5258-814-5.
- Anand Ranjan, O. P. Singh, G. R. Mishra, Himanshu Katiyar, “Review of Wireless fading and Outage Probability Evaluation for the Nakagami-Q (Hoyt) Fading Channel, Page”, Proceeding of International Conference on Emerging Trends in Electrical, Electronics & Sustainable Energy Systems (ICETEESES-2016), Page No. 48-55, ISBN No: 978-93-85-77-32-5
- Swati Mishra, G.R.Mishra, Rajinder Tiwari, O.P.Singh “Study on Transform Domain Algorithm of Digital Watermarking” Proceeding of National Conference on Advanced Computational Science and Technology (NCACT-2016), pp. 192-196, (During 4-5 March, 2016), ISBN No978-93-84224-49-3.
- Shazia Afreen, Vartika Singh, Vandana Dubey, O.P.Singh, G.R.Mishra “A Study on Quantum Gates, Quantum Circuits and Quantum Computation” Proceeding of National Conference on Information Security Challenges (NCISC-2016), pp. 33-36, (24th Feb, 2016), ISBN No: 978-93-8504-35-4.
- Swati Mishra, G.R.Mishra, Rajinder Tiwari, O.P.Singh “A Theoratic Study on the Spatial Domain Approach for Digital Watermarking” Proceeding of National Conference on Information Security Challenges (NCISC-2016), pp. 93-97, (24th Feb, 2016), ISBN No: 978-93-8504-35-4.
- Rohit Bhargava, G.R. Mishra “ZigBee and RFID Merging Technology in the Coal Mine Safety,” in Proceeding of National Conference on Contemporary Computing and Informatics (NCCI-2015), pp. 244-248, (During 8-9 May, 2015), ISBN No:978-93-84224-25-7.
- Rajinder Tiwari, G.R.Mishra, Sachin Kumar “An Introduction to the Smart Materials & Structures in Design of Intelligence based Applications: A Study ” in Proceeding of National Conference on Recent Advances in Chemical and Material Sciences (NCRACMS-2015), pp. 72-79, (During 23-24 Feb, 2015), ISBN No: 978-93-84224-25-7.
- Rajinder Tiwari, G.R.Mishra, Sachin Kumar, R.K.Singh “An Innovative Approach of Study of the Performance of DC Motors using LabVIEW” Proceeding of Ist International Online Conference on Research in Science, Engineering & Management (IOCRSEM-2014), pp. 477-486, ISBN No. 978-93-5174-703-1.
- Rajinder Tiwari, O.P.Singh, G.R.Mishra, Sachin Kumar, “Impact on Cells Mobility on Human-being Due to Exposure to EMF Radiation” Proceeding of National Conference on “Recent Trends in Communication System and Devices(NCRTCD-2012)” organized by Department of Electronics & Communication

Engineering, School of Engineering & Technology, Radha Govind Group of Institutions, Meerut (U.P) University of Lucknow , pp.287-293, (During 01-02 September, 2012).

- Rajinder Tiwari, Ganga Ram Mishra, “Consequences of High Frequency & EMF Radiation to Human Exposure: A Literature Overview” Proceeding of National Seminar Wireless Vision “A Global Prospective & Issues” organized by IETE Lucknow Centre, pp. 161- 166 (During 23-24 Sept, 2011).
- Rajinder Tiwari, Ganga Ram Mishra and Ashok Kumar Mishra, “Recent Developments in the Technology of Semiconductor Based Sensors for the Detection of Natural Gases: A Study” Proceedings of 16th National Seminar on Physics and Technology of Sensors (NSPTS-16), organized by Department of Physics, University of Lucknow, pp 379-387, (During 11-13 Feb, 2011), ISBN: 978-93-800-61-6.

Book Chapter:

- Shatrughna Ojha, Vandana Shukla, OP Singh, GR Mishra, RK Tiwari, “A Novel Approach for 4-Bit Squaring Circuit Using Vedic Mathematics”, Smart Innovations in Communication and Computational Sciences. Advances in Intelligent Systems and Computing, vol 1168., Editors: Tiwari S., Trivedi M., Mishra K., Misra A., Kumar K., Suryani E., Published by **Springer Nature (Scopus Indexed)**, Published Date: 02 August 2020, pp 177-184.
- Anand Ranjan, O. P. Singh, G. R. Mishra, Himanshu Katiyar, “Multi-objective-Based Lion Optimization Algorithm for Relay Selection in Wireless Cooperative Communication Networks”, Smart Innovations in Communication and Computational Sciences. Advances in Intelligent Systems and Computing, vol 1168., Editors: Tiwari S., Trivedi M., Mishra K., Misra A., Kumar K., Suryani E., Published by **Springer Nature (Scopus Indexed)**, Published Date: 02 August 2020, pp 69-77.
- Vandana Shukla, O.P. Singh, G.R. Mishra and R. K. Tiwari, “Design of Array Multiplier Circuit Using Reversible Logic Approach with Optimized Performance Parameters”, Smart Healthcare for Disease Diagnosis and Prevention 1st Edition, Editors: Sudip Paul and Dinesh Bhatia, Published by Academic Press, **Elsevier (Scopus Indexed)**, Published Date: 31st January 2020, pp115-124.
- Vandana Shukla, O.P. Singh, G.R. Mishra and R. K. Tiwari, “A Novel Approach for Reversible Realization of 4×4 Bit Vedic Multiplier Circuit”, Advances in VLSI, Communication, and Signal Processing Part of the Lecture Notes in Electrical Engineering book series (LNEE, volume 587), Editors: Dutta, D., Kar, H., Kumar, C., Bhadauria, V., Published by **Springer Nature (Scopus Indexed)**, Published Date: , pp 733-745, 2019.
- Sunil Kumar Ojha, O.P. Singh, G.R. Mishra and P. R. Vaya, “Analysis of SRAM Cell for Low Power Operation and Its Noise Margin”, Advances in VLSI, Communication, and Signal Processing, Part of the Lecture Notes in Electrical Engineering book series (LNEE, volume 587) Editors: Dutta, D., Kar, H., Kumar, C., Bhadauria, V., Published by **Springer Nature (Scopus Indexed)**, Published Date: , pp 733-745, 2019.
- R.P.Tripathi, Ankita Tiwari, G.R.Mishra, Dinesh Bhatia “Design and Implementation of Wearable Real Time ECG Monitoring System Based on Smart Phone”, Engineering Vibration, Communication and Information Processing, Lecture Series in Electrical Engineering, **Springer Nature (Scopus Indexed)**, 2018, PP. 495-503
- R.P.Tiwari, G.R.Mishra, “A Smart Approach to Estimate the Involvement of a Student during E-Learning Programme”, Revamping of Education System Through New education Policy (NEP),Editors: Mala Tandon, Sushanta K.Roul, Ritu T. Chakravarty & Dipita Bhattacharya Gangan Deep Publications, New Delhi: ISBN No. 978-81-88865-67-3
- Vandana Shukla, O.P.Singh, G.R.Mishra, R.K.Tiwari, “Application of VSMT gate for the Optimization of Multiplexer Circuit using Reversible Gate”, Emerging Trends in Information Technology, Editor Dr. R.A.Khan, Shroff Publisher & Distributers Pvt. Ltd. Book Chapter (Chapter No: 14): ISBN No: 13:978-93-5213-026-9
- Rajinder Tiwari, G.R.Mishra, Sachin Kumar, “A Statistical Mathematical Modeling of Fault Detection System for the Distribution System of Power Based Applications” Emerging Trends in Information

Technology, Editor Dr. R.A.Khan, Shroff Publisher & Distributers Pvt. Ltd. Book Chapter (Chapter No: 14): ISBN No: 13:978-93-5213-026-9.

Book:

- Raghuvendra Pratap Tripathi, G.R.Mishra, Dinesh Bhatia, “ECG Analysis and Cardiac Arrhythmia Prediction using FDST and ANNs ”, Published by Lambert Academic Publishing
- Ganga Ram Mishra, “Circuit & Systems”, Techguru Publications.
- Ganga Ram Mishra, “Digital Electronics”, Techguru Publications.
- Ganga Ram Mishra, “Signals & Systems”, Techguru Publications.

Paper Presented in International/National Conference/Seminar:

- “A Futuristic Approach for Improved Renewable Energy System with Reversible Logic Approach”, presented in 2nd International Conference on Sustainable Technologies for Environmental Management (STEM-2019), organized by, Department of Environmental Engineering, Delhi Technical University, New Delhi (25-26 March, 2019).
- “Study of Role of Smart Materials in Consumer Electronics” presented in TEQIP-III Sponsored National Conference on Smart Materials, Devices and Sustainable Technologies (SMDST-2019), 15-16 March , 2019 organized by , Department of Applied Science, MMM University of Technology, Gorakhpur (during 15-16 March , 2019).
- “Study of Embedded Securities for IOT Applications” presented in 3rd International Conference on Challenges and Opportunities of Technological Innovation in India (COTII-2019) organized by , Ambalika Institute of Technology & Management (AITM), Lucknow, (during 22-23 February , 2019).
- “Design and FPGA Implementation of Bluetooth Based Home Automation System” presented in National Symposium on Advance Materials Science organized by , Department of Physics, DDU Gorakhpur University, Gorakhpur, (during 7-8 December , 2018).
- “A Brief on Applications of PLC and SCADA in Industrial Automation” presented in National Conference on Recent Trends in Engineering Science, Management and Pharmacy (NCRTESMP-2018) organized by , Institute of Engineering & Management, Gorakhpur, (during 17-18 November , 2018).
- “Application of ICT in E-learning” presented in National Seminar on Digital Learning Opportunities and Challenges in Higher Education Institutes (NSDLOCHET-18) organized by , Department of Physics, BSNV Post Graduate College, Lucknow, (on 05 May, 2018).
- “Simulation Study of CMOS Compound Pair Amplifier” presented in International conference on Recent Innovations in Science and Engineering organized by, Buddha Institute of Technology, Gorakhpur, (during 01-02 April, 2018).
- “Study on Role of Renewable Energy Sources in Environmental Protection” presented in National Workshop on Solar Energy and Its Impact on Environment”, Organized by Department of Electronics & Communication Engineering, Amity School of Engineering & Technology, Amity University Lucknow Campus, (during 12-13 September , 2017).
- “Study of Leakage Control Transistor and Multi-Threshold CMOS Techniques for Low Power CMOS Applications” presented in 2nd National conference on Innovative Researches in Engineering & Technology organized by, Buddha Institute of Technology, Gorakhpur, (during 25-26 March, 2017).
- “Study of the Techniques Used for Replicating the Human Olfactory System” presented in 2nd National conference on Innovative Researches in Engineering & Technology organized, Buddha Institute of Technology organized by, Gorakhpur, (during 25-26 March, 2017).

- “IOT Based Smart Health Monitoring System” presented in International Symposium on “Computational and Characterization Techniques in Engineering & Sciences, organized, by Jointly organized by Department of Electronics & Communication Engineering, Department of Physics & Department of Electrical Engineering under the aegis of HRDC, Integral University, Lucknow, (30 March, 2017).
- “Study of GDI Techniques for Low Power Dissipation and Less Transistor Count” presented in International Symposium on “Computational and Characterization Techniques in Engineering & Sciences, organized, by Jointly organized by Department of Electronics & Communication Engineering, Department of Physics & Department of Electrical Engineering under the aegis of HRDC, Integral University, Lucknow, (30 March, 2017).
- “Modeling & Simulation of D-Flip Flop using AVL Technique for Low Power Application” presented in International Conference on “Challenges & Opportunities for Technological Innovation in India (COTII-2017), organized by Ambalika Institute of Technology & Management (AITM), Lucknow(30 March, 2017).
- “Application Based Study of IOT for Vehicle Simulation System” presented in International Conference on “Challenges & Opportunities for Technological Innovation in India (COTII-2017), organized by Ambalika Institute of Technology & Management (AITM), Lucknow, (during 3-4 March, 2017).
- “Study of Techniques used in Design and Fabrication of Nano Electrodes for Bio-Signal using CNT/PDMS” presented in 6th National Conference on Nanomaterials and Nanotechnology (NATCON NAMTECH VI), organized by Department of Physics, University of Lucknow, Lucknow, (during 11-12 February, 2017).
- “Design of a Low Power Wearab, Wireless SoC Based Sensor for Continuous Monitoring of ECG” presented in International Conference on Structure and Dynamics of Biomolecules, organized by Department of Physics, DDU Gorakhpur, and Gorakhpur (during 27-28 January, 2017).
- “Study of Biosensor for ECG Signal Monitoring using Wireless Technology” presented in International Conference on Structure and Dynamics of Biomolecules, organized by Department of Physics, DDU Gorakhpur, and Gorakhpur (during 27-28 January, 2017).
- Study of Various Spectrum Sensing Technique: Overview” presented in National Conference on Advances in System and Technology, organized by Institute of Technology and Management, GIDA, Gorakhpur during 22-23 April, 2016.
- “An Innovative Model for Monkey Repellent Using Ultrasonic Wave” presented in National Conference on Advances in System and Technology, organized by Institute of Technology and Management, GIDA, Gorakhpur during 22-23 April, 2016.
- “A Study on Linear Feedback Shift Register with its Architecture Application & Implementation” presented in National Conference on Information Security Challenges (NCISC-2016), organized by Department of Information Technology, Babasaheb Bhimrao Ambedkar University, Lucknow, on 24th February, 2016.
- “Study on Invisible Watermarking Technique for Digital Image” presented in 4th National Conference on Challenges & Opportunities for Technical Innovation in India (COTTI-2016) organized Ambalika Institute of Management & Technology, Lucknow, during 19-20, Feb, 2016.
- “Study on Energy Efficient Sensor Network using Ad-Hock Cognitive Radio” presented in 4th National Conference on Challenges & Opportunities for Technical Innovation in India (COTTI-2016) organized Ambalika Institute of Management & Technology, Lucknow, during 19-20, Feb, 2016.

- “Study of Quantized FIR Filter using CSD Technique” presented in 4th National Conference on Challenges & Opportunities for Technical Innovation in India (COTTI-2016) organized Ambalika Institute of Management & Technology, Lucknow, during 19-20, Feb, 2016.
- “A Comparative Study of Microstrip & Plasma Antenna” presented in International Conference on Plasma Science, Technology & Application 2016 organized by Amity School of Applied Sciences, Amity University Lucknow Campus, during 20-21 January, 2016.
- “Design of 3-Bit SMT Gate Using Reversible Logic” presented in National Conference on Contemporary Computing and Informatics (NCCI-2015), organized by Madan Mohan Malviya University of Technology, Gorakhpur, during 08-09 May, 2015.
- “Large Bandwidth Low Power Two Stage CMOS Operational Amplifier” presented in SRMS International Conference on Science and Technology 2015 “Exploring the Potential of Convergence” organized by SRMS College of Engineering & Technology, Bareilly, during 27-28 February, 2015.
- “Reversible Logic Technology: A New Approach of Low Power VLSI Design” presented in 3rd National Conference on Challenges & Opportunities for Technical Innovation in India (COTTI-2015) organized Ambalika Institute of Management & Technology, Lucknow, on 14th, Feb, 2015.
- “Future of Cast Optimized Technology” presented in 3rd National Conference on Challenges & Opportunities for Technical Innovation in India (COTTI-2015) organized Ambalika Institute of Management & Technology, Lucknow, on 14th, Feb, 2015.
- “Design of DRAM having Dummy Cell Sensing Structure” presented in National Conference on Recent Advances in Electronics & Computer Engineering (RAECE-2015) organized by Department of Electronics & Communication Engineering and Department of Computer Engineering, IIT Roorkee, during 13-15 February, 2015
- “Role of E-Education for the Enhancement of Higher Education” presented in IInd International Conference on Higher Education: Issues & Challenges, organized by College of Education Bilaspur, Greater Noida, G.B. Nagar during 8-9 December, 2014.
- “ECG Waveform Data Extraction using K-means method” presented in National Conference on Integrated Circuits and Signal Processing, organized by Amity School of Engineering & Technology, Amity University Lucknow Campus, during 25-26 September, 2014.
- “Power Analysis of Dual port Double Ended SRAM” presented in National Conference on Integrated Circuits and Signal Processing, organized by Amity School of Engineering & Technology, Amity University Lucknow Campus, during 25-26 September, 2014.
- “A Theoretic Study of Steganography Techniques for Data Hiding” presented in SCES-2014, organized by IEEE Student Branch & Motilal Nehru National Institute of Technology, Allahabad, during 28-30 April, 2014.
- “An Application Based Study of different Image Compression Technique” presented in 2nd National Conference on Challenges & Opportunities for Technical Innovation in India (COTTI-2013) on 22th, Feb, 2013 organized Ambalika Institute of Management & Technology, Lucknow.
- “Study and Comparison of Different Techniques of Data Hiding in Image” presented in 2nd National Conference on Challenges & Opportunities for Technical Innovation in India (COTTI-2013) on 22th, Feb, 2013 organized Ambalika Institute of Management & Technology, Lucknow.

- “Device Modeling and Circuit Design Challenges of Finfet for Ultra Low Power VLSI Applications” presented in 2nd National Conference on Challenges & Opportunities for Technical Innovation in India (COTTI-2013) on 22th, Feb, 2013 organized by Ambalika Institute of Management & Technology, Lucknow.
- “Study of Encrypted Data Hiding Techniques in image Based on Redundant Bit” presented in National Conference on Advances in System & Technologies (NCAST-2014), organized by Institute of Technology & Management, GIDA Gorakhpur from 23rd – 24th, January, 2014.
- “Real SIM-85- A Virtual Lab for Microprocessor Applications” presented in National conference on E-Learning: Tools, Technology and Current Trends organized by Maharaja Agarsain Institute of Technology Ghaziabad from 01 – 02, March, 2013.
- “Study of Simulated Virtual Experiment Design Tools” presented in National conference on E-Learning: Tools, Technology and Current Trends organized by Maharaja Agarsain Institute of Technology Ghaziabad from 01st – 02nd, March, 2013.
- “Novel Design of Optimized Multiplexer Circuit using Reversible Logic” presented in National Conference on Challenges & Opportunities for Technical Innovation in India (COTTI-2013) on 16th, Feb, 2013 organized by Ambalika Institute of Management & Technology, Lucknow.
- “MEMS Sensor for Measurements of Sensitivity & Temperature” presented in National Conference on Challenges & Opportunities for Technical Innovation in India (COTTI-2013) on 16th, Feb, 2013 organized by Ambalika Institute of Management & Technology, Lucknow.
- “Emerging Trends in Quantum Cryptography”, presented in National Seminar on Cyber Security: Challenges and Opportunities jointly organized by Department of Computer Science, DDU Gorakhpur University Gorakhpur and National Institute of Electronics & Information Technology, Gorakhpur, pp. 08, (During 19-20 Feb, 2012).
- “Overview: Improvement in Current Mirrors using Wilson Scheme” presented in National Seminar on Recent Advances in Digital and Analog Signal Processing jointly organized by Department of Electronics, DDU Gorakhpur University and National Institute of Electronics & Information Technology, Gorakhpur, pp. 20, (During 21-22 Feb, 2012).
- “Speech Compression Techniques” presented in National Seminar on Recent Advances in Digital and Analog Signal Processing jointly organized by Department of Electronics, DDU Gorakhpur University and National Institute of Electronics & Information Technology, Gorakhpur, pp. 26, (During 21-22 Feb, 2012).
- “Design and Comparative Study of Various Digital Adders” presented in National Seminar on Recent Advances in Digital and Analog Signal Processing jointly organized by Department of Electronics, DDU Gorakhpur University and National Institute of Electronics & Information Technology, Gorakhpur, pp. 23, (During 21-22 Feb, 2012).
- “Microcontroller Based Intelligent Flood Alert System” presented in 16th National Seminar on Physics and Technology of Sensors (NSPTS-16), organized by Department of Physics, University of Lucknow, pp 284-287, (During 11-13 Feb, 2011).
- “3G Mobile Communication: A Review” presented in National Seminar and Exhibition on “Emerging Applications on Next Generation Networks organized by IETE Lucknow Centre, pp 104-107, (During 23-24 Jan, 2010).

- “Theoretical Modeling of InAsSb Based Photovoltaic Detector for Free Space Optical Communication” presented in National Seminar and Exhibition on “Emerging Applications on Next Generation Networks organized by IETE Lucknow Centre, pp 104-107, (During 23-24 Jan, 2010).
- “Highly Linear Transconductor Structure for Nano-Scale CMOS Technology” presented in 2nd National Conference on Nanomaterials and Nanotechnology organized by Department of Physics, University of Lucknow, pp 304-308, (During 21-23 Dec, 2009).
- “DNA Detection Technique for Security” presented in Seminar Cum Workshop on “Communications – 2020” organized by IETE Lucknow Centre in Association with Amity University Lucknow Campus (During 08-09 March, 2008).
- “Simulation Studies of CMOS Current Mirror Circuit” presented in National Conference on “Nano,Bio and Information Technology Integration” during March 23-25, 2007 Organized by Sanjay Institute of Engineering & Management Mathura.

Invited Lectures:

:

- Delivered Invited Talk and Resource Person in Workshop on “Signal Processing Tool & Techniques” Organized by Institute of Engineering & Technology, Dr. RML Avadh University, Faizabad (During 5-6 January, 2018) under TEQIP-III .
- Delivered Invited Talk on Matlab and Its Application in 6th National Conference on Nanomaterials and Nanotechnology (NATCON NAMTECH VI), organized by Department of Physics, University of Lucknow, Lucknow (during 11-12 February, 2017).
- Deliver Invited talk on Role of ICT in Digital India during Digital India Week (an initiative of Department of Electronics & Information Technology (DeitY), Ministry of communication and Information Technology) from 1st July -7th July 2015 at National Institute of Electronics and Information Technology (NIELIT), Lucknow on 4th July, 2015.

Short Term Course/Faculty Development Program/Workshop/Seminar Attended:

- Attended IUAC Acquaintance Programm and One Day National Workshop on “Accelerator Based Science Research” organized by Department of Physics & Electronics, Dr. RML Avadh University, Ayodhya, (on 16th July, 2020, Online Mode).
- Attended Five Day Online Short Term Training Programme on "Internet of Thing (IoT) and its Application in Industry” Jointly organized by Department of Instrumentation Engineering Ramrao Adik Institute of Technology, Navi Mumbai and B & R Automation Pvt. Ltd. Pune (During 08th -12th June, 2020, Online Mode).
- Attended in webinar on "General Concepts in Embedded Systems Design” Organized by Department of Electronics Communication Engineering, Organized by Feroze Gandhi Institute of Engineering & Technology, Raebareli (on 09th June, 2020, Online Mode).
- Attended Five Day Online Short Term Training Programme on "Signal Analysis, Machine Learning and its Applications using Python and MATLAB” Jointly organized by Department of Electronics & Communication Engineering Ramrao Adik Institute of Technology, Navi Mumbai and IETE- RAIT (During 01th -05th June, 2020, Online Mode).
- Attended Faculty Development Program (FDP) on the topic "Trends in Technological Intelligence-T²I 2020" organized by IEI Chapter of Department Electronics & Communication Engineering & Department of Electrical Engineering, BBDNIIT in association with The Institution of Engineers (India) (During May 31 to June 01, 2020, Online Mode).

- Attended Faculty Development Program on “Recent Trends in High Frequency Devices & Application (RTHFDA-19)”, organized by Department of Electronics Engineering, IET, Lucknow, (17th – 21 th June, 2019).
- Attended Joint AKTU-AICTE Short Term Training Programme for Faculty Development Program on “Advances in Microwave Engineering (AME-19)”, organized by Dr. APJ Abdul Kalam Technical University, Lucknow, (8th – 12 th June, 2019).
- Attended Professional Development Programme on “Ethical Issues in Research”, organized by Amity Institute of Biotechnology, Amity University, Lucknow Campus, (16th – 17 th January, 2019).
- Attended Faculty Week Faculty Development Program on “Ensuring Excellence in Teaching/Learning/Research in Higher Educational Institutions Using ICT (ID)” organized by Department of Information Technology, Babasaheb Bhimrao Ambedkar University, Lucknow, (22nd December, 2018 – 06 th January, 2019).
- Attended Faculty Development Programme & Workshop on “Statistical Methods in Biological Data Analysis” organized by Amity Institute of Biotechnology, Amity University Uttar Pradesh, Lucknow Campus, (during 02-06 July, 2018).
- Attended Short Term Course on “Emerging Research Areas in Engineering & Technology through ICT” Conducted by Department of Electrical Engineering, BBDNITM, Lucknow, (during 04-08 June, 2018).
- Attended Faculty Development Program on “Advanced Digital Image Processing Techniques (ADIP-18)” under TEQIP-III organized by Department of Computer Science & Engineering, Institute of Engineering & Technology, an Autonomous Constituent Institute of Dr. APJ Abdul Kalam Technical University, Lucknow, (23-27 February, 2017).
- Attended National Workshop/Seminar on “Importance of Technical Terminology in Mathematics and Its Application”, organized by Commission for Scientific and Technical Terminology, Department of Higher Education, Ministry of Human Resource Development, Government of India at Nehru Gram Bharati University, Allahabad (during 16—18 February, 2018).
- Attended One Week Short Term Course on “MATLAB and LABVIEW with Its Hardware Interface” through ICT mode organized at BBDNITM, Lucknow, (during 29 January-02 February, 2018).
- Attended Workshop on “Information Storage Management and Introduction to Big data Analytics” organized by Department of Computer Science & Engineering, Amity School of Engineering & Technology, Amity University Uttar Pradesh, Lucknow, (during 09-16 January, 2018).
- Attended Workshop on “Application of Mathematics” organized by Amity School of Applied Sciences, Amity University Uttar Pradesh, Lucknow, (on 22 December, 2017).
- Attended Industrial Workshop on “Essentials of NX Designers” organized by Department of Mechanical Engineering, Amity University Uttar Pradesh, Lucknow, (22-28 September, 2017).
- Attended Faculty Development Programme on “System Modeling & Simulation and Big Data Analytics” organized by Department of CSE ASET, Amity University Uttar Pradesh, Lucknow, (1-8 June, 2017).
- Attended One Week FDP on “Basic Course on ANSYS FEA & CFD” organized by Jointly organized by Amity University Uttar Pradesh, Lucknow Camps & MINDBOX Evolving Education a Channel partner of ANSYS, (28 Feb – 4 March, 2017).
- Attended Industrial Workshop on PLC & SCADA, organized by Department of ECE ASET, Amity University Uttar Pradesh, Lucknow, (18 – 19 January, 2017).
- Attended Faculty Development Programme on “Research Methods and Scientific writing” organized by Amity Institute of Information Technology, Amity University, Lucknow Campus, Amity University (20-21December, 2016).

- Attended National Seminar on “Emerging Trends in Non-Conventional Energy Resources” Lucknow organized by Department of Electronics & Communication Engineering, Department of Electrical Engineering and Department of Physics, Integral University, (22 October 2016).
- Attended Workshop on “Advancements In Wired & Wireless Networks” organized by Amity Institute of Information Technology, Amity University, Lucknow Campus, Amity University (11 – 12 August, 2016).
- Attended one Week Faculty Development Programme on "Networking Simulator (NS-2)" organized by Department of Computer Science & Engineering, ASET, Amity University (During May 24-31, 2016).
- Attended One Day seminar on “Modeling, Simulation and Control of Mechatronic Systems”, organized by Department of Electronics & Communication Engineering, Amity School of Engineering & Technology Amity University, Lucknow Campus (on 17th , March, 2016).
- Attended National Workshop on “Power Electronics (NWPE-2015) ” organized by Department of Electrical Engineering, Delhi Technological University Delhi under the aegis of NaMPET-II, an initiative of DeitY. (During 06-07, November, 2015).
- Attended Workshop on “Intellectual Property Rights: Issues & Challenges” organized jointly by Indira Gandhi National Open University & University of Lucknow, Lucknow (on 21th -July, 2015).
- Attended a Complimentary Seminar on “MATLAB & Simulink for Engineering Education” organized by MathWork India at Lucknow (on 9th -July, 2015)
- Attended Interactive Faculty Development Program organized by Amity University Uttar Pradesh, Lucknow Camps. (June 30-July, 06, 2015).
- Attended one Week TEQIP-II sponsored Faculty Development Programme on "Recent Advances and Challenges in Power & Energy for Sustainable Growth" organized by Department of Electrical Engineering, Delhi Technological University Delhi under the aegis of NaMPET-II, an initiative of DeitY. (During June 1 – 5, 2015).
- Attended National Instructional Workshop on Cryptology organized by Department of Mathematics Motilal Nehru Institute of Technology Allahabad. (During June 5-9, 2014).
- Attended a Workshop on “E-Learning Tools for Teaching” organized by Maharaja Agarsain Institute Technology Participant (3rd March, 2013).
- Attended a Science Academics Lecture Workshop on “Nanotechnology and its Application” organized by Department of Applied Science, M.M.M. Engineering College , Gorakhpur jointly sponsored by Indian Academy of Sciences, Bangalore, Indian National Science Academy (During 18-20, March, 2013).
- Attended one day National Workshop on “Nano Science & Engineering” organized by Department of Electronics Engineering, K.N.I.T. Sultanpur on 26 May 2012.
- Attended National Workshop on “Recent Advances in Micro-Electro-Mechanical Systems RAMEMS-2011” organized by MEMS Design Center (Under NAMPASS) Institute of Technology, BHU, Varanasi during (07-09 March 2011) .
- Attended a one Day Workshop on “VLSI, EDA & Embedded Tools & Technology in Education” organized by Sandeepani School of VLSI and Embedded Software Design, at Institute of Engineering & Technology Lucknow. (On 14th November, 2010).
- Attended a one week Faculty Development Program on “Embedded System Design Using Microcontrollers” organized by Amity School of Engineering & Technology, Amity University Rajasthan by Instrument Society of India, Indian Institute of Sciences Bangalore (During 07-12 Dec. 2009).

- Attended a Workshop on “Nanotechnology in Semiconductor Industry” organized by Department of Electronics & Communication Eng., M.M.M. Engineering College Gorakhpur (During 05-06 April 2008).
- Attended a Workshop on “Embedded System Design Based on Microcontroller” organized by Department of Electronics Engineering, Institute of Engineering & Technology Lucknow. (During 28-29 Feb. 2008).
- Attended a Workshop on “Advanced Optoelectronics Materials & Devices (AOMD-07)” organized Centre for Research in Microelectronics (CRME), Department of Electronics Engineering, Indian Institute of Technology BHU Varanasi. (During 27-29 Dec 2007).
- Attended a Workshop on “VLSI Design Tools” organized by Department of Electronics Engg., Institute of Engineering & Technology Lucknow. (During 17-18 Nov. 2007).
- Attended a Workshop on “Circuit simulation Using SPICE and VHDL” organized by Department of Electrical Engg., Motilal Nehru National Institute of Technology, Allahabad, Sponsored by Technical Education Quality improvement Program (TEQIP) during 8-10 April 2006.
- Attended a short term Course on “Design of DSP Based Work Station” organized by Department of Electrical Engg. Indian Institute of Technology Kanpur. (During 18-23 July 2005). In this course we study in ADSP 21xx architecture, Assembly Language Programming & Design steps in ADSP 21xx based system Design.

Editorial Board Member/Reviewer:

- Member of Editorial Board of International Journal of Research in Electrical & Electronics Engineering.
- Reviewer, IEEE Access, The Multidisciplinary Open Access Journal (A Scopus Indexed Journal), Published by IEEE.
- Reviewer, IETE Journal of Research (A SCI Indexed Journal) Published by Taylor & Francis.
- Reviewer, Journal of Engineering Science & Technology (IJSTEC), (A Scopus Indexed Journal), Published by Taylor's University, Malaysia.
- Reviewer, International Journal of Electronics, (A SCI Indexed Journal), Published by Taylor & Francis.
- Reviewer, IEEE International Conference on Emerging Trends in Computing and Communication Technologies (ICETCCT-2017), Organized by Department of Electronics & Communication Engineering and School of Computing, Graphic Era Hill University, Dehradun
- Reviewer, International Conference on Emerging Trends in Electrical, Electronics & Sustainable Energy Systems (ICETEESES-16), Jointly Organized by Department of Electrical Engineering and Department of Electronics Engineering, KNIT Sultanpur, U.P.
- Reviewer, 4th International Conference on Eco-friendly Computing and Communication Systems (ICECCS) 7th - 8th December, 2015, NITK, Kururkshetra, Haryana, India.
- Reviewer, Sixth International Conference on Computer and Communication Technology ICCCT 2015 September 25 -27 2015, Organized by Department of Computer Science & Engineering Motilal Nehru National Institute of Technology Allahabad.
- Member Editorial Board of National Conference on 4th National Conference on “Challenges & Opportunities for Technological Innovation in India (COTII-2016)”.
- Member Editorial Board of National Conference on 3rd National Conference on “Challenges & Opportunities for Technological Innovation in India (COTII-2015)”

Seminar/Conference Organized:

- Coordinated Webinar by Prof. Satyadrata Jit , Professor, Department of Electronics and Engineering , IIT,BHU Varanasi on “Introduction to Tera Hertz Technology” organized by Department of Electronics & Communication Engineering, Amity School of Engineering & Technology, Amity University Uttar Pradesh, Lucknow Campus (on 01 May,2020).
- Coordinated Webinar by Dr. Gaurav Trivedi , Associate Professor, Department of Electronics & Electrical Engineering , IIT Guwahati on “Journey of Microprocessor: Past, Present and Future” organized by Department of Electronics & Communication Engineering, Amity School of Engineering & Technology, Amity University Uttar Pradesh, Lucknow Campus (on 02 May,2020).
- Coordinated Webinar by Dr. Manish Tiwari , Associate Professor, Department of Electronics & Communication Engineering , MNNIT Allahabad, Prayagraj on “General Concepts of Embedded System Design” organized by Department of Electronics & Communication Engineering, Amity School of Engineering & Technology, Amity University Uttar Pradesh, Lucknow Campus (on 17April, 2020).
- Coordinated Webinar by Dr. P. K. Saxena, CEO & CTO, Tech Next Lab Pvt Ltd (TNL) “Electron Transport Properties of AlxGa1-xN/GaN Transistors Based on Solution of Boltzmann-Equation Monte Carlo Simulation” organized by Department of Electronics & Communication Engineering, Amity School of Engineering & Technology, Amity University Uttar Pradesh, Lucknow Campus (on 10th April,2020).
- Member Technical Programm Committee of “2nd International Conference on VLSI, Communication and Signal Processing (VCAS-2019)” organized by Department of Electronics & Communication, MNNIT, Allahabad (During 21-23 October, 2019).
- Coordinated Workshop on “Electro-Magnetic Radiation (EMR) Awareness” jointly organized by Department of Electronics & Communication Engineering, Amity School of Engineering & Technology (ASET) and Department of Telecommunication (DOT), U.P. (East) (on 26th September 2019).
- Member Technical Programm Committee of “International Conference on VLSI, Communication and Signal Processing (VCAS-2018)” organized by Department of Electronics & Communication, MNNIT, Allahabad (During 29 November-1 December, 2018).
- Member Organizing Team of “Data Visualization Using R (DVUR)” organized by Department of Computer Science & Engineering, Amity School of Engineering & Technology, Amity University Uttar Pradesh, Lucknow Campus (During 04-05 April, 2018).
- Member International Advisory Board of “International Conference on Recent Innovations in Science and Engineering-18(ICRISE-18)”, Organized by Buddha Institute of Technology, Gorakhpur (During 31th March-01 April, 2018)
- Member International Advisory Board of 2nd International Conference on “Challenges & Opportunities for Technological Innovation in India (COTII-2017)”, Organized by Ambalika Institute of Technology & Management (AITM), Lucknow (During 23-24 February, 2018).
- Member National Advisory Committee of “Student Research Convention-East Zone (Anvenshan-2018) organized by Association of Indian Universities, New Delhi, Hosted by, Institute of Engineering & Technology, Dr. RML Avadh University, Faizabad (During 24-25 February, 2018).
- Member International Advisory Board of 2nd International Conference on “Challenges & Opportunities for Technological Innovation in India (COTII-2017)”, Organized by Ambalika Institute of Technology & Management (AITM), Lucknow (During 23-24 February, 2018).

- Member International Advisory Board of “International Conference on Recent Innovations in Science and Engineering-18(ICRISE-18)”, Organized by Buddha Institute of Technology, Gorakhpur (During 31th March-01 April, 2018)
- Member Technical Program Committee, 1st International Conference on Engineering Vibration, Communication and Information Processing (ICoEVCI-2018), Organized by Manipal University, Jaipur (During 09-10, March, 2018).
- Member Technical Program Committee, IEEE International Conference on Emerging Trends in Computing and Communication Technologies (ICETCCT-2017), Organized by Department of Electronics & Communication Engineering and School of Computing, Graphic Era Hill University, Dehradun (During 17-18, November, 2017).
- Member Organizing Team of “AI: Boon or Bane” organized by Department of Electronics & Communication Engineering, Amity School of Engineering & Technology, Amity University Uttar Pradesh, Lucknow Campus (On 06 September, 2017).
- Member Organizing Team of “Solar Energy and Its Impact on Environment” organized by Department of Electronics & Communication Engineering, Amity School of Engineering & Technology, Amity University Uttar Pradesh, Lucknow Campus (During 04-05 April, 2018).
- Coordinator, Two Days Industrial Workshop on PLC & SCADA organized by Department of Electronics and Communication Engineering, Amity School of Engineering and Technology in association with Logicon Automation and Praveen Electrical Appliances (18- 19th January, 2017).
- Member Organizing Committee, Workshop on “Opportunities in Wireless Communication” organized by Department of Electronics and Communication Engineering, Amity School of Engineering and (on 20th Feb, 201)
- Member National Advisory Board of National Conference on Advances in Systems and Technologies, Organized by Institute of Technology & Management, GIDA, Gorakhpur (During 22-23 April, 2016)
- Member National Advisory Board of National Conference on Recent Trends in Engineering & Technologies, Organized by Buddha Institute of Technology, GIDA, Gorakhpur (During 12-13 March, 2016).
- Member Organizing Committee of Seminar-Cum-Workshop on Optical Communication organized by Amity School of Engineering & Technology, Amity University Lucknow Campus in association with ITI Naini (during April, 09-10, 2015).
- Member Organizing Committee of Workshop on Industrial Automation using PLC & SCADA organized by Amity School of Engineering & Technology, Amity University Lucknow Campus in association with Logicon Automation & IETE (On 31st October , 2014).
- Member Organizing Committee of National Conference on “Integrated Circuit & Signal Processing” organized by Department of Electronics & Communication Engineering, Amity School of Engineering & Technology, Amity University Lucknow Campus (during September, 25-26, 2014).
- Coordinator, Symposia – 2014 on “Recent Trends in Information, Communication & Technology (RT-ICT-2014)” organized jointly by IETE Lucknow Center & Amity School of Engineering and Technology (ASET) and IETE Students Forum (ISF), Amity, at Amity University Uttar Pradesh, Lucknow on 01-02-2012
- Member Organizing Committee in National Seminar on “Rural Empowerment through Broadband” organized by IETE Lucknow Centre, (During 16-17 March, 2013).

- Coordinator, Symposia – 2012 on “Emerging Trends in Communication and Information Technology” organized jointly by IETE Lucknow Center & Amity School of Engineering and Technology (ASET) and IETE Students Forum (ISF), Amity, at Amity University Uttar Pradesh, Lucknow on 01-02-2012
- Coordinator, Symposia – 2011 on “Emerging Trends in Electronics, Computers & Information Technology” organized jointly by IETE Lucknow Center & Amity School of Engineering and Technology (ASET) and IETE Students Forum (ISF), Amity, at Amity University Uttar Pradesh, Lucknow on 01-02-2012
- Member Organizing Committee in National Seminar on Wireless Vision “A Global Prospective & Issues” organized by IETE Lucknow as co-opted member and member of Publication Committee (During 23-24 Sept, 2011).
- Member Organizing Committee in National Seminar and Exhibition on “Emerging Applications on Next Generation Networks organized by IETE Lucknow Center (during Jan, 23-24, 2010).
- Member Organizing Committee in 3rd National Conference on “Nanomaterials & Nanotechnology” jointly organized by Amity School of Engineering & Technology, Amity University Lucknow Campus and Department of Physics, University of Lucknow (during Dec, 21-23, 2010).

Membership:

- Life Membership of IETE New Delhi.
- Life Member of Material Research Society of India(MRSI)
- IET U.K.
- Life Member, Indian Science Congress

Technical/Computer Skills/Exposure:

Assembly Language	:	8085, 8086, Micro Controller 8031/8051, 89C 51
Programming Language	:	C, VHDL (View Logic, Xilinx, Mentor Graphics)
VLSI Simulation Tool	:	ModelSim, View Logic, DFT (Design Fault Test)
VLSI Synthesis Tool	:	Leonardo Spectrum, Xilinx,
DSP Tool	:	MATLAB
Package	:	SPICE, TASM
Circuit Simulation Tools	:	Simulink, TINA, Multisim, Commsim, Microcap 7, Microsim
Embedded System Tools	:	Hardware/Software Simulator of Microcontroller 8031/8051, Emulator of Micro Processor 8085, Micro Controller 8051, Universal Programmer
Circuit Design	:	Digital Circuit & Logical Design, Simulation of Digital, Circuit using VHDL, Synthesis of Circuit using Leonardo Spectrum & their FPGA Implementation.
System Design	:	Digital and Microprocessor/Micro controller Based System Design.
Platforms	:	Windows 9X, MS DOS

PERSONAL PROFILE:

Name : **Ganga Ram Mishra**
Father's Name : Shri Rammurti Mishra
Date of Birth : 01 Dec. 1977
Nationality : Indian
Correspondence Address : S/O Shri Rammurti Mishra
Village- Marha
Post- Barhya Chowk
Dist- Gorakhpur-273165

Date:

Place:

(Ganga Ram Mishra)