

CURRICULAM VITAE

Shiksha Jain

Assistant Professor and H.O.D
Department of Electronics and Communication
Institute of Engineering and Technology,
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OBJECTIVE: I am seeking an opportunity to associate myself with an institution that believes “Knowledge is main power and strength” and that can offer me challenging and exciting job.

AREA OF RESEARCH: VLSI DESIGN, DIGITAL ELECTRONICS.

PROFESSIONAL EXPERIENCE

Total Teaching Experience - 13 YEARS

| S. No. | Organization | Designation | From | To |
|--------|---|---------------------------|---------------------------------|----------------------------|
| 1 | IET, Dr. RML Avadh University, Ayodhya, U.P. | Assistant Professor | 15 th June 2018 | Till Date |
| 2 | IET, Dr. RML Avadh University, Faizabad, U.P. | Guest Lecturer | 14 th September 2010 | 14 th June 2018 |
| 3 | Rajkiya Mahila Polytechnique, Faizabad, U.P. | Guest Lecturer | 10 th August 2009 | 30th June 2010 |
| 4. | Marathwada Institute of Engineering and Technology, Bulandshahr, U.P. | Lecturer and Head of Labs | 10 th August 2005 | July 2007 |

PROFESSIONAL EDUCATION

| S. No. | Name of Examination | Board/University | Year of passing | Subject/Branch | Percentage/CGPA/Grade/CPI |
|--------|--------------------------------------|---|---|--|---------------------------|
| 1 | High School | UP Board, Allahabad, U.P. | 1997 | Science, Math, English | 57% |
| 2 | Three Year Poly-Technique. (Diploma) | UPTB Lucknow, U.P. | 2001 | Modern Consumer Electronics Engineering | 84% |
| 4. | B. Tech | UPTU, Lucknow, U.P. | 2005 | Electronics and Communication Engineering | 66.77% |
| 5 | M. Tech | GBTU, Lucknow, U.P. | 2011-2012 | Electronics Engineering (Digital Electronics and System) | 68.40% |
| 6 | M.B.A | Dr. Rammanohar Lohia University Ayodhya, U.P. | 2013 | Finance and Marketing | 72.32% |
| 7 | Ph.D in Electronics | Dr. Rammanohar Lohia University Ayodhya, U.P. | Course Work Completed in 2017. Enrolled in Feb 2018. | | |

COMPUTER LITERACY

CADENCE Software

**180nm CMOS
Technology**

Matlab Software

Model Sim Se-Ee 5.4 A Software

Xilinx 1SE 6 Software

IT (Information Technology)

(Six Month Certificate Course From CMC Ltd)

**EXCELLENT “A”
Grade**

RESEARCH PROFILE

A- Research Papers Published in Refereed Journals/Other Reputed Journals as notified by UGC

| S.N o. | Author | Title Name | Journal Name | Year of Publication |
|-------------------|---|--|---|--------------------------------|
| 1 | Raj Kumar Tiware and Shiksha Jain ” | Second Order Low Pass RC Active Filter for Low Frequency Signal Processing | UGC Approved Journal International Journal for Research in Engineering Application & Management (IJREAM), Journal-64077 Volume 06, Issue 03, Impact Factor-6.466, ISSN: 2454-9150 | July 2020 |
| 2 | Raj Kumar Tiware and Shiksha Jain | CMOS Active Filter for Dual Purpose like Low Pass and High Pass Filter for Biomedical System | UGC Approved Journal International Journal of Research in Electronics and Computer Engineering (IJRECE) A UNIT OF I2OR, Vol. 8, Issue 2, PP. 203-208, ISSN: 2393-9028 (PRINT) ISSN: 2348- 2281 (ONLINE). | April-June 2020, |
| 3 | Raj Kumar Tiware, Shiksha Jain, and Gaya Prasad Tiware “ | Complementary Compound Push Pull Power Amplifier for Wide Frequency Band Applications using CMOS Nanotechnology,” | IEEE Xplore | June 2020 |
| 4 | Raj Kumar Tiware and Shiksha Jain | Complementary Compound Wideband Amplifier for Broadband Communication System, | Scopus Indexed International Journal of Advanced Research in Engineering and Technology (IJARET) Volume 11, Issue 6, PP. 585-592, Article ID: IJARET_11_06_053, DOI: 10.34218/IJARET.11.6.2 020.053, © IAEME | June 2020. |

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| | | | Publication, | |
| 5 | Shiksha Jain | A Review: Performance Analysis of Voltage Controlled Differential Ring Oscillators Based On Delay Cell Using CMOS Nanotechnology,” | UGC Approved Journal International Journal of Research and Analytical Reviews (IJRAR), Vol.7, Issue 1, Journal No : 43602) & 5.75 Impact Factor , E-ISSN 2348-1269, P- ISSN 2349-5138, Page No. 149-154 | February 2020. |
| 6 | Raj Kumar Tiwari, Gaya Prasad, Shiksha Jain, Ganga Ram Mishra, Monika Tiwari and Parul Trivedi, | RKTG Pair Amplifier with Gain Boosting Stage | American International Journal of Research in Science, Technology, Engineering & Mathematics, PP.23-26, AIJRSTEM 19-106; | 2019 |
| 7 | Gaya Prasad, Raj Kumar Tiwari, Shiksha Jain and Ganga Ram Mishra, | Simulation Study of CMOS Compound Pair Amplifier | International Journal of Advance Research in Science and Engineering Volume No. 07, Special Issue No. 01, Impact Factor (2018): 2.83. ISSN No.(o) 2319-8354, (P) 2319-8346. | April 2018 |

B- Papers Presented in International Conferences

| S No | Title of Paper | Title of Conference | Organized By | Month / Year | Level |
|------|---|---|---|--------------|---------------|
| 1 | Implementation of Digital Filter Using Radix 2 ⁿ Modified Booth Algorithm on Advanced Programmable Technique | International Symposium on Recent Trends in Electronics and Communication (ISRTEC) | Kamala Nehru Insititute of Technology, Sultanpur, U.P | Nov 2012 | International |
| 2 | Overview of DSP Implementation on Vertex-4 FPGA Kit | International Symposium on Recent Trends in Electronics and Communication (ISRTEC) | Kamala Nehru Insititute of Technology, Sultanpur, U.P | June' 2020 | International |
| 3 | A Study of Low Power High Speed Fully Differential Active Filter Using CMOS Amplifier | 22 nd Internatinal Conference of International Academy of Physical Sciences (CONIAPS -XXII) | Dr. Rammanohar Lohia Avadh University, Ayodhya | April 2018 | International |
| 4 | Simulation Study of CMOS Compound Pair Amplifier | International Conference on Recent Innovations in Science and Engineering | MMM University Gorakhpur | April 2018 | International |
| 5 | Qualitative Analysis of RKTG Pair Amplifier | 23 rd Internatinal Conference of International Academy of Physical Sciences (CONIAPS -XXIII) | Nepal | Nov 2018 | International |
| 6 | The Review Study of Low Input Low Frequency CMOS Amplifier for Biomedical Application | 62 nd Annual IETE Convention (AIC2K19) International Conference on Industry 4.0 | I.E.T, Dr. R.L.A. University, Ayodhya associated with Institute of Electronics and Telecommunication Engineers (IETE) | Sept 2019 | International |
| 7 | Low Power Complementary Compound Push Pull Power Amplifier with Very Wide Frequency Band Using CMOS Technology | 62 nd Annual IETE Convention (AIC2K19) International Conference on Industry 4.0 | I.E.T, Dr. R.L.A. University, Ayodhya associated with Institute of Electronics and Telecommunication Engineers (IETE) | Sept 2019 | International |
| 8 | Complementary Compound Push Pull | International Conference on Electrical and | M.M.M. University of Technology, Gorakhpur (UP) | Feb 2020 | International |

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| | Power Amplifier for Wide Frequency Band Applications using CMOS Nanotechnology,” | Electronics Engineering (ICF3-2020) | India and North Dakota State University, Fargo USA | | |
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c- Papers Presented in National Conferences / Seminars

| S No | Title of Paper | Title of Conference/ Seminar | Organized By | Month/ Year | Level |
|------|--|--|--|-------------|----------|
| 1 | Fully Parallel Implementation of Direct Form Digital FIR Filter Using Radix-4 VHDL Description | National Conference on Emerging Trends in Electrical and Electronics Engineering | Kamala Nehru Insititute of Technology, Sultanpur, U.P | Nov 2011 | National |
| 2 | Review Study of DSP Implementation in Advanced Programmable Architecture | National Conference on Advances in Computer Communication and Embeded System | Kamala Nehru Insititute of Technology, Sultanpur, U.P | April 2012 | National |
| 3 | Optimization Performance of CMOS Amplifier for Radio Frequency Application | National Conference on Emerging Trends in Engineering and Technology (NCETET-2019) | Dr. Rammanohar Lohia Avadh University, Ayodhya | July 2019 | National |
| 4 | High Frequency and Low Power S-CMOS Oscillator for PLL | National Seminar on Recent Advances in Meterial Science and Electronics (RAMSE-2019) | Physics and Electronics Department of Dr. Rammanohar Lohia Avadh University, Ayodhya | Feb 2019 | National |

d- Seminars / FDP /Workshops // PDT attended

| S No | Name of Title | Lable | Organized By | Period |
|------|--|---------|--|---------|
| 1 | “Intellectual Property Rights Significance and Protection Process” December-2013 | Seminar | Dr. R.L.A. University Ayodhya and Council of Science and | One Day |

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| | | | Technology U.P | |
| 2 | “Summer Training Program on Active Learning for senior Faculty” Knowledge Incubation for TEQIP, July 2-6, 2018 | FDP | I.I.T. Kanpur | 05 Days |
| 3 | “AICTE Model Curriculum and CBCS”, 31 st August-1 st September, 2018 | Workshop | I.E.T., Dr. R.L.A. University Ayodhya under the aegis of TEQIP –III, NPIU, Ministry of Human Resource Development , Gov. of India | 02 Days |
| 4 | “Robotics Advance Control with Brainwave Technology (RACBT-2019)” March 10-14, 2019. | Workshop | Department of IT, I.E.T., Dr. R.L.A. University Ayodhya under the aegis of TEQIP IIIrd | 5 Days |
| 5 | “Internet of Things” March 10-14, 2019. | FDP | I.E.T., Dr. R.L.A. University Ayodhya under the aegis of TEQIP IIIrd | 5 Days |
| 6 | “Android APP Development” March 10-14, 2019. | Workshop | I.E.T., Dr. R.L.A. University Ayodhya under the aegis of TEQIP IIIrd | 5 Days |
| 7 | “Role of Matrix Laboratory (MATLAB) in Advanced Communication” March 15-19, 2019 | FDP | I.E.T., Dr. R.L.A. University Ayodhya under the aegis of TEQIP IIIrd | 5 Days |
| 8 | “Recent Advances in CAD/CAM” March 25-29, 2019 | Workshop | I.E.T., Dr. R.L.A. University Ayodhya under the aegis of TEQIP IIIrd | 5 Days |
| 9 | “5G and Industrial Internet of Things”, April 1-6, 2019 | National Workshop | I.E.T, Dr. R.L.A. University Ayodhya under the aegis of TEQIP IIIrd | 5 Days |
| 10 | “NBA Accreditation From Output to Outcome” 12 th April, 2019. | Workshop | Under the aegis of NPIU and organized by SPIU | One day |
| 11 | “Machine Learning in IOT: Implementation and Use Cases” June 24 th -28 th , 2019. | National Workshop | I.E.T., Dr. R.L.A. University Ayodhya under the aegis of TEQIP IIIrd | 5 Days |
| 12 | FDP on “Enhancing the Quality on Teaching, Learning and Research” | FDP | I.E.T., Dr. R.L.A. University Ayodhya | 5 Days |

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| | July 21-25, 2019 | | under the aegis of TEQIP IIIrd | |
| 13 | National Workshop on Outcome Based Education and NBA Accreditation for Programme, Aug 2-6, 2019 | FDP | I.E.T., Dr. R.L.A. University Ayodhya under the aegis of TEQIP IIIrd | 5 Days |
| 14 | “Digital Invention for Accreditation and Enhancement of Knowledge Skill and Employability” 18 th October -2019. | Workshop | Under the aegis of NPIU and organized by SPIU | One Day |
| 15 | “Smart City and Modernized Construction”, Dec 2 nd - 6 th , 2019. | National Workshop | I.E.T., Dr. R.L.A. University Ayodhya under the aegis of TEQIP IIIrd | 5 Days |
| 16 | “Research Methodology” | National Workshop | IQAC, Dr. R.L.A. University, Ayodhya in Collaboration with Research for Resurgence Foundation (RFRF), Nagpur | 5 Days |
| 17 | “E-content creation for MOOCs and MOODLE” | FDP | Dr. R.L.A. University Ayodhya under the aegis of TEQIP IIIrd & Spoken Tutorial IIT Bombay | 5 Days |

E- Webinar (Online Seminar) /Short Term courses

| S No. | Name of Title | Lable | Organized By | Period |
|-------|--|------------------|---|--------|
| 1 | “Significant Role of E- education through Social Distancing during Lockdown of COVID-19 Panademic” held on 8 th -9 th , 2020 | National Webinar | I.E.T., Dr. R.L.A. University, Ayodhya (UP) with A.I.T Bangalore collaboration with SPIU, U.P | 2 Day |

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| 2 | “Next Generation Computing System” held on 17 th May 2020 | International Webinar | MCA Department, I.E.T., Dr. R.L.A. University, Ayodhya (UP) | One Day |
| 3 | “Online Courses and Course Design using Knimbus Digital Library” held on 21 st May 2020 | Webinar | Powered by Knimbus associated with I.E.T., Dr. R.L.A. University Ayodhya, (UP) | One Day |
| 4 | “Impact of COVID-19 on Indian Economy, Ecology and Environment” held on 05/06/2020 | National Webinar | Department of Commerce and Management in Collaboration with IGTAMSU, Zero, Arunachal Pradesh (INDIA) | One Day |
| 5 | “Technical Manuscript Writing and Publishing in Reputed Journal” held on 22 nd June, 2020 | Live Online National Webinar | Engineering Staff College of India, Gachibowli, Hyderabad | One Day |
| 6 | Microprocessor and Microcontroller | Short Term Course | NPTEL Online Certification Course, IIT Kharagpur | 12 Week |
| 7 | CMOS Digital VLSI Design | Short Term Course | NPTEL Online Certification Course, IIT Kharagpur | 8 Week |

EXPERIENCE OF ADMINISTRATIVE WORK

- >- Working as Assistant Professor and HOD of ECE Department at I.E.T., Dr. R.L.A. University Ayodhya, (UP) from September 2019 to till date.
- >- Worked as a committee member of “Dean Student Welfare” of I.E.T, Dr. R.L.A. University Ayodhya, (UP) from 2019 to till date.
- >- Worked as a member of NBA Committee I.E.T, Dr. R.L.A. University Ayodhya, (UP) from 2019 to till date.

- >- Worked as a Committee member of “E-Library Development” of I.E.T, Dr. R.L.A. University Ayodhya, (UP) from 2019-2021.

FINAL YEAR RESEARCH PROJECTS

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| Ph.D Electronics | Circuit Design and Simulation Oo Low Power Low Noise High Speed Amplifiers using CMOS Technology. (In this project, we have design four types of CMOS amplifier- two for small signal amplifier and other two for large signal amplifier. Designed Low power low noise complementary compound wide band amplifier and Low frequency low power low noise complementary compound active low pass filter use for a Small signal amplification. In this research, low input high power complementary compound push pull and low power low noise complementary compound VCR oscillator use for large signal amplifications. These amplifiers are based on complementary compound pair as a transistor). |
| M.B.A Finance and Marketing | Risk Management Regarding Working of a Broking Firm and its Inventors |
| M.Tech Electronics Engineering | VHDL Implementation of Direct Form Digital FIR Filter (In this project we design FIR filter using radix-4 booth multiplier on VHDL description and simulate on FPGA Kit and compare this FIR filter with the Radix-2 and Radix-2 multiplier and array multiplier). |
| B.Tech Electronics and Comm. Engg | FM Radio Station and Communication System (It's use as a reception of the FM signals. It contains of FM Tx, FM Rx, and Music cassette system. This operates on very high frequency 30 to 300 MHz and high relilbty better stability, etc.). |
| Three Years Diploma Modern Cconsumer Electronics | Multipurpose Digital Counter (Its use to count the nountber of pulses in binary numbers. Its 4 digit counter with 9.9 MHz and contains ICs 7447-BCD to seven segment decoder, ICs-7490-Decade Counter and seven segment Display). |

ADDITIONAL SKILLS

- >- To read and solve the technical and non- technical problems.
- >- Highly motivated and able to coordinate students activities.
- >- Management Skills.
- >- Play the role of Organizing Committee Member in many events like Alumni Meet organized by Engineering Alumni Association, National Science Day organize by I.E.T. Ayodhya etc.
- >- Achieve many 1st and 2nd prizes in sports Activities at college level.
- >- Achieve 5 belts in Karate Martial Arts by International Martial Arts Federation Sports India.

PERSONAL DETAILS

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|---------------------------|--|
| Date of Birth | : 15 July 1984 |
| Father's Name | : Sri Devendra Kumar Jain |
| Husband Name | : Late Gaurav Jain |
| Children | : Two(One boy and girl) |
| Language Known | : English and Hindi |
| Religion & Caste Category | : Jain Hindu, General |
| Permanent Address | : 3/2/62, Mukeri Tola, Rekab Ganj, Ayodhya |

DECLARATION

I, Shiksha Jain, hereby declare that the information contained herein is true and correct to the best of my knowledge and belief.

(SHIKSHA JAIN)