



NISHANT SINGH  
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### OBJECTIVE

To get a platform where I can explore and enhance my field of interest thoroughly and become well known professor by contributing to the existing knowledge in my field.

### QUALIFICATION

Course	School/college	Board/ university	Year of passing	Marks(%) /CGPA
Ph.D	Dr. RML Avadh University	Dr. RML Avadh University	Pursuing	-
M.Tech. (VLSI)	ABV-Indian Institute of Information Technology and Management, Gwalior	An autonomous institute of MHRD GOI	2017	7.39/10
B.Tech. (ECE)	Institute of engineering and technology	Dr. RML Avadh University Faizabad	2013	75.42/100
Intermediate	Tiny Tots Senior Secondary School Faizabad	Central Board of Secondary Education	2007	57.60/100
High School	Tiny Tots Senior Secondary School Faizabad	Central Board of Secondary Education	2005	69.40/100

### GATE SCORE

- Qualified GATE-2014,2015,2018.

### TRAINING

- Worked as trainee for one month at “Doordarsan kendra/Broadcasting corporation of india” Lucknow under the supervision of Dr.Ravindra Nathani (Head of broadcasting dept.)
- Training/Workshop done at “Labsguru technologies” for six weeks during that I had designed many embedded system based projects.

### ACADEMIC PROJECTS

- **Ultrasonic Distance Measure Robot [ULTRA-4]- Major Project**  
This is a robot which performs many actions, such as it gives the actual position of wall or obstacle which comes in front of it by measuring the distance and displayed it on 7-segment and also show the images of the objects(moving or stationary) by camera.
- Designed a 32word x 32bit and 136word x 32bit “Register file” project of Govt. of India.
- Designed an ultrasonic sensor based Robot for obstacle detection under water namely “ULTRA 4” using microcontroller.
- Designed a Line Follower Robot, FM receiver, LDR based street lite controller.

### M.Tech. THESIS

- Title : Process Variation Aware CMOS and FinFET based Low Power Address Decoder
- Supervisor : Dr. Manisha Pattanaik and Prof G.K. Sharma
- Description : Proposed a new address decoder circuit with low power consumption and process variation tolerance using 32nm and 180nm technology. Using Adaptive pre-charge technique the overall power consumption is reduced to a large extent. Process variation effects on the circuit have been minimised by using back gate biasing on double gate FinFET. This decoder is to be integrated with other modules to make one 32word X 32bit register file architecture for high speed processor under an SMDP project funded by Department of Electronics and Information Technology (Deity).

### RESEARCH PUBLICATIONS

- Research paper : Two (Communicated)

a) Nishant singh, “Low power high performance Address Decoder for 32word x 32-bit register file.” (ESTIJ).

b) Nishant singh, “Circularly Polarized Antenna for Wireless Communication Application.” (WALAILAK).

- Research paper : Published

a) Nishant singh, Ruby, “WIMAX Model Performance Analysis and Design in Fading Channel.” (IJRDASE), volume 20, Issue 2, 2020, Page 1-8.

#### KEY SKILLS

- **EDA Tools-** Cadence (Virtuoso), HSPICE (Synopsis), C-designer (Synopsis), Xilinx (Mentor graphics), Gateway (Silvaco), Smart Spice (Silvaco), DeckBuild (Silvaco).
- Immense knowledge of electronics subjects such as Electronics devices and circuits, Analog Circuits, Digital Electronics, Microprocessors based systems, MOS for VLSI.
- Documentation tool Latex (Kiel)
- Basic programming in C-Language.

#### ACHIEVEMENTS

- Was a volunteer in IEEE conference organized at IIITM, Gwalior.
- Won 2<sup>nd</sup> prize in chess in sports fest, at Dr. RML Avadh university, Faizabad
- Participated in technical quiz “Cognizance” held at BHU, Varanasi.`
- Participated in many events as a group leader held at HBTI, Kanpur.
- Hold the position of captain in school volleyball team.

#### STRENGTHS

- Presentation skills
- Communication skills
- Interpersonal skills
- Team Spirit

#### HOBBIES

- Playing guitar, listening music
- Sports: Volleyball and Chess .

#### PERSONAL DETAILS

- Date of Birth : 05-24-1990
- Father’s Name : Mr. Sunil kumar singh
- Permanent address : 5/15/62 jamuniabagh chowk (Faizabad) U.P.-224001
- E-mail : [nishantsingh2490@gmail.com](mailto:nishantsingh2490@gmail.com), er.nishantsingh@outlook.com
- Mobile : 7275728609

#### DECLARATION

I hereby declare that all the information provided above is correct to the best of my knowledge.

Date:

Place: Faizabad

**NISHANT SINGH**